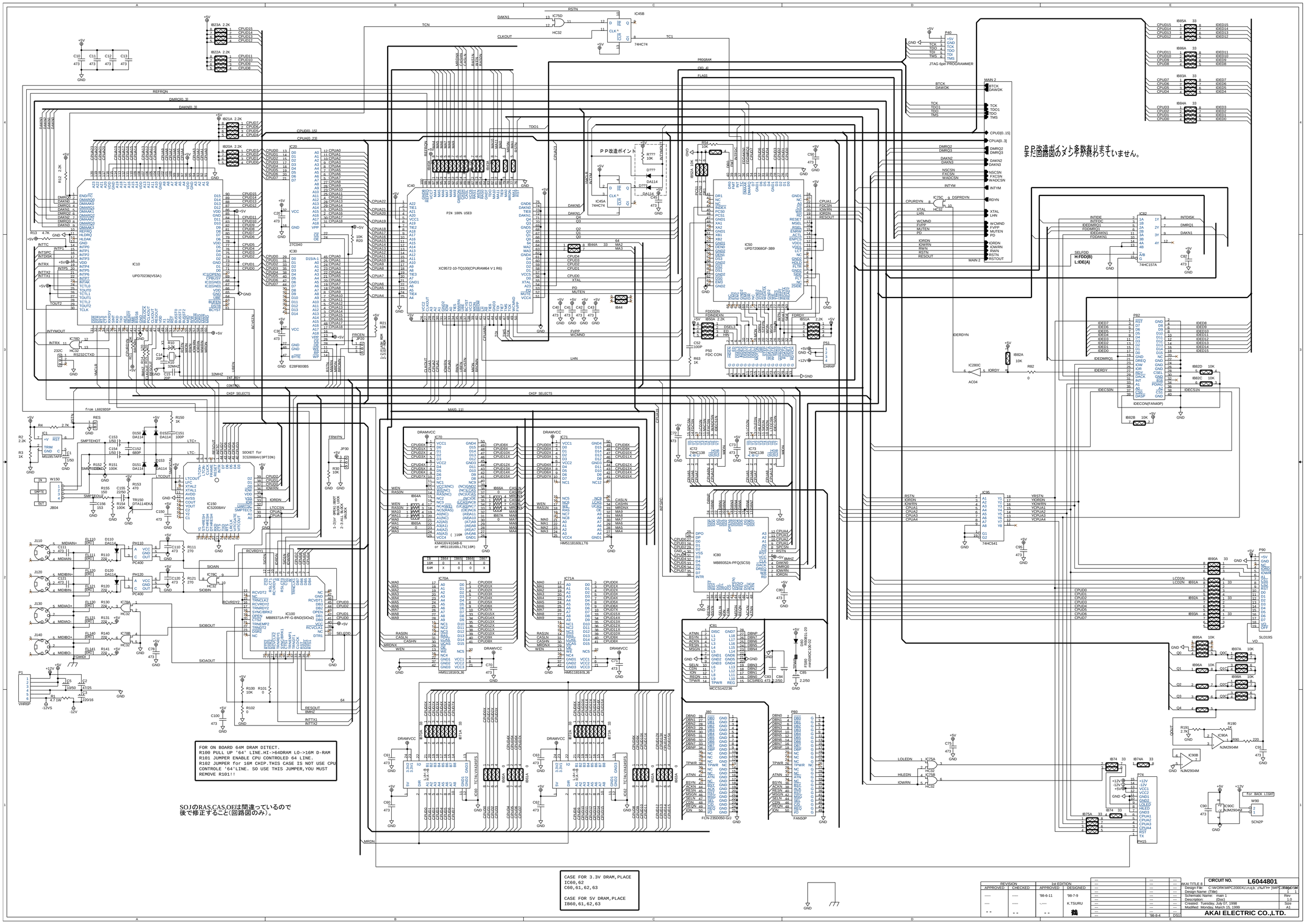
P.P改造箇所のメンテ多数有ります。
また改造箇所のメンテ多数有りますていません。
P.P改造ポイント
PIN 100% USED
XC9572-10-TQ100(CPURAM64 V 1 R6)
IC10
UPD70236(VS3A)
IC50
UPD72068GF-3B9
IC80
M889352A-PFQ(SCSI)
IC100
M88R3371A-PF-G-BND(SIO&gn)
IC150
ICS2008AV
IC70A
HM5118160LLT6
IC71A
HM5118160LJ6
KM416V4104B-6
or HM5118160LLT6(16M)
HM5118160LJ6
IC70
IC71
IC20
IC30
IC40
IC45B
IC45A
IC75C
IC75A
IC75B
IC78C
IC78A
IC78B
IC78D
IC280C
IC90B
IC90C
IC95
IC81
IC82
IC72
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IC74
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IB174A
IB175A
P40
JTAG 6pin PROGRAMMER
MAIN 2
P50
FDC CON
P82
IDECON(FAN40P)
74HC157A
74HC541
74HC138
P90
SLD19S
P74
PH15
P80
FAN50P
FCN-235D050-G/J
MCCS142236
P1
VHRGP
J110
J120
J130
J140
J3B04
W150
W90
SCN2P
EHR4P
PS1
PC40
PH110
PH120
CIRCUIT NO. L6044801
AKAI ELECTRIC CO.,LTD.
Design File: C:\WORK\MPC2000XL\ULB_CMOTH+ 2000-05-04
Design Name: (Title)
Schematic Name: main 1
Description: (Doc)
Created: Tuesday, July 07, 1998
Modified: Monday, March 15, 1999
APPROVED
CHECKED
1st EDITION
DESIGNED
'98-6-11
'98-7-9
K.TSURU
'98-8-4
FOR ON BOARD 64M DRAM DITECT.
R100 PULL UP '64' LINE.HI->64DRAM LO->16M D-RAM
R101 JUMPER ENABLE CPU CONTROLED 64 LINE.
R102 JUMPER for 16M DRAM.THIS CASE IS NOT USE CPU CONTROLE '64'LINE. SO USE THIS JUMPER,YOU MUST REMOVE R101!!
SOJのRAS,CAS,OEは間違っているので
後で修正すること(回路図のみ)。
CASE FOR 3.3V DRAM,PLACE
IC60,62
C60,61,62,63
CASE FOR 5V DRAM,PLACE
IB60,61,62,63